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COFC

Docket No. : SON-1351/DIV  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Patent No. 6,808,999 B2

Inventor: Hiroyuki Miwa

Patented: October 26, 2004

ATTN: CORRECTIONS BRANCH

For: METHOD OF MAKING A BIPOLAR TRANSISTOR HAVING A REDUCED BASE  
TRANSIT TIME

**REQUEST FOR CERTIFICATE OF CORRECTION**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**Certificate**  
**FEB 04 2005**  
**of Correction**

Sir:

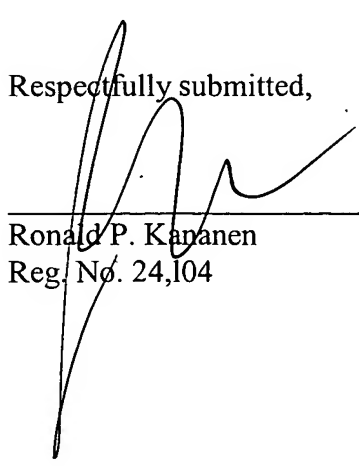
It is respectfully requested that a Certificate of Correction be issued in connection with the above-identified patent. It appears that a mistake(s) was recorded through the fault of the Patent and Trademark Office in the printing of the patent as clearly disclosed by the records of the Office within the meaning of 35 USC § 254. Accordingly, two copies of the special Certificate of Correction are attached hereto. It is believed that the error(s) noted is an error(s) of consequence involving claim language and thus warrants the granting of a Certificate of Correction. It is believed that the error(s) were made on the part of the Office and that no government fee is required.

Should any costs be incurred, please consider this authorization to charge Deposit  
Account No. 18-0013.

Dated: February 1, 2005

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Respectfully submitted,

  
\_\_\_\_\_  
Ronald P. Kananen  
Reg. No. 24,104

**7 FEB 2005**

UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION

Patent No. 6,808,999 B2

Dated: October 26, 2004

Inventor (s): Hiroyuki Miwa

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 8

Line 56, "bas" should read - - base - - .

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MAILING ADDRESS OF SENDER:

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Patent No. US 6,808,999 B2

FORM PTO 1050 (REV. 3-82) SON-2778

7 FEB 2005